

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD OF FABRICATING A BIPOLAR JUNCTION TRANSISTOR
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Jing-Horng Gau</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: NAUP0536USA</p>	
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<p>Submitted By:</p> <p>Winston Hsu Registered Number: 41,526</p>	<p>Elec. Sign.</p> <p>Winston Hsu</p>

Documents being submitted:	Files
us-request	NAUP0536-usrequ.xml us-request.dtd us-request.xsl
us-assignment	NAUP0536-usassn.xml us-assignment.xsl us-assignment.dtd NAUP0536ASS1.tif
us-declaration	NAUP0536-usdecl.xml us-declaration.dtd us-declaration.xsl
us-fee-sheet	NAUP0536-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-power-of-attorney-grant	NAUP0536-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-declaration	NAUP0536DEC1.tif
us-declaration	NAUP0536DEC2.tif
application-body	NAUP0536-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Naup0536usa-01.tif Naup0536usa-02.tif Naup0536usa-03.tif

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Comments